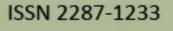
# Advanced Science and Technology Letters

The ASTL series is committed to the publication of proceedings of Advanced Science and Technology. Its objectives is to publish original research in various areas of Science and Technology. This will provide good chances for academic and industry professionals to discuss recent progress in areas of Science and Technology.

Research papers were strictly peer-reviewed by program committees to make sure that the the papers accepted were high quality and relevant to the current and future issues and trends in Science and Technology.

The scope of ASTL includes the entire area of science and technology from the current and future trends. The Language of publication is English. The Authors have to sign the SERSC ASTL copyright transfer form.



ASTL

SERSC

Slezak Wai-chi Fang Sabah Mohammed Young-hoon Lee Jong-an Park (Eds.)

ASTL 19

Advanced Researches

on Software Technology

SoftTech

2013

Advanced Science and Technology Letters ASTL 19

# Advanced Researches on Software Technology

Proceedings International Conference, SoftTech 2013 Yeosu, South Korea, April 2013

SERSC

Science and Engineering Research Support soCiety Proceedings

# Advanced Researches on Software Technology SoftTech 2013

# April 26-27, 2013 Hiddenbay Hotel, Yeosu, Korea



Volume Editors:

Dominik Slezak, Warsaw University & Infobright, Poland

Sabah Mohammed, Lakehead University, Canada

Young-hoon Lee, Honorary President of KIIT Society, Hannam University, Korea

Jong-an Park, President of KIIT Society, Chosun University, Korea

Wai-chi Fang, National Chiao Tung University, Taiwan

# Copyright © Science and Engineering Research Support soCiety (SERSC) All rights reserved

Copyright and Reprint Permissions: Abstracting is permitted with the credit to the source. Other copying, reprint request should be addressed to: SERSC Copyrights Manager, Head Office: 20 Virginia Court, Sandy Bay, Tasmania, Australia, Phone no.: +61 3 9016 9027, Fax no.: +82-42-367-0859

ISSN 2287-1233

### Organization

#### **Honoarary Chair**

Young-hoon Lee, Honorary President of KIIT Society, Hannam University, Korea

#### **General Co-Chairs**

Jong-an Park, President of KIIT Society, Chosun Unviersity, Korea Wai-chi Fang, National Chiao Tung University, Taiwan

#### **Program Co-Chairs**

Dominik Slezak, Warsaw University & Infobright, Poland Sabah Mohammed, Lakehead University, Canada

#### **Publicity Co-Chairs**

Tao Jiang, Huazhong University of Science and Technology, China June Verner, University of New South Wales, Australia Silvia Abrahao, Camino de Vera, Spain Byungjoo Park, Hannam University, Korea Muhammad Khurram Khan, King Saud University, KSA Hsi-Ya Chang (Jerry), National Center for High Performance Computing, Taiwan Yun-sik Kwak, Communication Unviersity, Korea

#### **International Advisory Board**

Aboul Ella Hassanien, Cairo University, Egypt Akingbehin Kiumi, University of Michigan-Dearborn, USA Alfredo Cuzzocrea, University of Calabria, Italy Byeong-Ho KANG, University of Tasmania, Australia Dae-sik Ko, Mokwon Unviersity, Korea Haeng-kon Kim, President of SERSC Society, Catholic University of Daegu, Korea Ha Jin Hwang, KIMEP, Kazakhstan Jose Luis Arciniegas Herrera, Universidad del Cauca, Colombia Tien N. Nguyen, Iowa State University, USA Dae-sung Kang, Donga Unviersity, Korea Gongzhu Hu, Central Michigan University, U.S.A Hideo KURODA, FPT University, Vietnam J. H. Abawajy, Deakin University, Australia Javier Garcia-Villalba, Universidad Complutense of Madrid, Spain Kirk P. Arnett, Mississippi State University, U.S.A Martin Drahansky, BUT, Faculty of Information Technology, Czech Republic Samir Kumar Bandyopadhyay, University of Calcutta, Kolkata, India Sankar Kumar Pal, Indian Statistical Institute, India Tadashi Dohi, Hiroshima University, Japan Tai-hoon Kim, GVSA and UTAS, Australia Tatsuya Akutsu, Kyoto University, Japan Wenbin Jiang, Huazhong University of Science & Technology, China

#### **Program Committee**

Abdelouahed Gherbi Ecole de Technologie Superieure (ETS), Canada Abdelwahab Hamou-Lhadj, Concordia University, Canada Agustin Yague Technical University of Madrid, Spain Ami Marowka, Shenkar College of Engineering and Design, Israel Ashfaqur Rahman CQUniversity, Australia Carmine Gravino, University of Salerno, Italy Chamseddine Talhi? Ecole de Technologie Superieure (ETS), Canada Chia-Chu Chiang, University of Arkansas at Little Rock, USA Chima Adiele, Trinity Western University, Canada Dinesh Verma, IBM, USA Doo-Hwan Bae, KAIST, Korea Emilia Mendes, University of Auckland, New Zealand Fausto Fasano, University of Molise, Italy Filip Orsag, BUT, Faculty of Information Technology, Czech Republic Gabriele Bavota Universita degli Studi di Salerno, Italy Giuseppe Scanniello, University of Basilicata, Italy Gongzhu Hu, Central Michigan University, USA Harvey Siy, University of Nebraska, USA Hironori Washizaki, National Institute of Informatics, Japan Hyeon Soo Kim, Chungnam University, Korea Istvan Siket, University of Szeged, Hungary Jennifer Perez Benedi, Technical University of Madrid, Spain Jin Wang, Nanjing University of Information Science & Technology, China Jiro Tanaka, University of Tsukuba, Japan Jonathan Lee, National Central University, Taiwan Jongmoon Baik, Korean Advanced Institute of Science Technology, Korea Jose L. Arciniegas, Universidad del Cauca, Colombia Joseph Balikuddembe, SANQUOTE Project, South Africa Juan Garbajosa Technical University of Madrid, Spain Karel Richta, Czech Technical University, Czech Republic Kendra Cooper, University of Texas at Dallas, USA Kin Fun LI, University of Victoria, Canada Kurt Wallnau, Carnegie Mellon University, USA Laszlo Vidacs, Hungarian Academy of Sciences, Hungary Laurence Duchien, University of Lille, France Lerina Aversano, University of Salerno, Italy Lirong Dai Seattle University, USA Luigi Buglione, Engineering.IT/ETS, Italy Maria Tortorella, University of Sannio, Italy Mokhtar Beldjehem, Sainte-Anne's University, Canada Morshed Chowdhury, Deakin University, Australia Olga Ormandjieva, Concordia University, Canada Praveen Ranjan Srivastava, Birla Institute of Technology & Science, India Rattikorn Hewett, Texas Tech University, USA Ricardo Campos, Polythenic Institute of Tomar, Portugal Rita Francese, University of Salerno, Italy Robert Glass, Griffith University, USA Robin Gandhi, University of Nebraska at Omaha, USA Rocco Oliveto, University of Salerno, Italy Rudolf Ferenc, University of Szeged, Hungary Salahuddin Al Azad CQUniversity, Australia Satoshi Takahashi University of Tsukuba, Japan Shawkat Ali, CQUniversity, Australia Silvia Abrahao, Universidad Politecnica de Valencia, Spain Takanori Terashima Miyagi University, Japan Tao Gong, Donghua University, China Tokuro Matsuo, Yamagata Universirty, Japan Vincenzo Deufemia, University of Salerno, Italy Wuwei Shen, Western Michigan University, USA Yijun Yu, The Open University, UK

Performance Analysis	anycore Systems Using OpenCL and Its ano, Phuong Thi Yen, Jeong-Gun Lee	.187
A Cost-Effective Local Ste Window	ereo Matching Based on the Unconventional	101
	on-Sik Son, Yong-Hwan Lee, Byungin Moon	.191
	ne Stereo Matching Hardware Architecture	.195
Systems, and Applications	omputing to Many-Task Computing: Challenges, Kim, Seoyoung Kim, Seungwoo Rho, Sangwan	.199
The AMGA WS-DAIR Im Sunil Ahn, Taesang Hul	plementation h, Soonwook Hwang	.203
Band-Rejection-Filter Effe Gwanggil Jeon, Young	ect Using Removed Spectral Boundary	.207
Bit Plane Approach for Wa Gwanggil Jeon, Young	atermarking -Sup Lee	.211
A Flexible Cantilever Bear Young-Sup Lee	m Control using a Tip Displacement Sensor	.215
A Frequency Domain Scra Gwanggil Jeon, Young	mbling Using Different Sized Empty Block	.219
	es based on Embedded Metadata	223

# A Cost-Effective Local Stereo Matching Based on the Unconventional Window

Kyeong-ryeol Bae<sup>1</sup>, Hyeon-Sik Son<sup>1</sup>, Yong-Hwan Lee<sup>2</sup> and Byungin Moon<sup>3,\*</sup>

<sup>1</sup> School of Electrical Engineering & Computer Science, Kyungpook National University, Daegu, Korea

{puris1, soc\_shs1984}@ee.knu.ac.kr

<sup>2</sup> School of Electronic Engineering, Kumoh National Institute of Technology, Gumi, Korea yhlee@kumoh.ac.kr

<sup>3</sup> School of Electronics Engineering, Kyungpook National University, Daegu, Korea bihmoon@knu.ac.kr

**Abstract.** This paper proposes an efficient stereo matching method using the window with the size of  $2^k$  by  $2^k$  that can be implemented in high-performance stereo matching systems. To evaluate the proposed method, various matching methods are modeled with C and implemented with an HDL. The experimental results show that the proposed method can reduce computational complexity without significant performance reduction compared with the conventional window based method.

Keywords: Stereo matching, Local matching, Depth, Window size.

#### 1 Introduction

The stereo matching is a search problem which finds the accurate correspondence points between the left and right images. If the camera model is given and the correspondence points are found, the depth information can be computed by triangulation [1].

Most visual systems studies based on the stereo matching use the difference-based local matching methods such as SAD (Sum of Absolute Differences) or SSD (Sum of Squared Differences) [2]. This is mainly because of the limitation of the hardware resources and computation complexity. These difference-based matching methods are simple to implement, but have high matching error rate due to the differences in the brightness of the images. Because of the disadvantage of difference-based stereo matching, the correlation-based local matching methods such as Census transform or NCC (Normalized Cross Correlation) have been studied [3]. These methods are robust to matching ambiguity caused by the noise and intensity change. However, the correlation-based local matching methods are time-consuming procedures because of the complexity of the functions. For real-time performance, a lot of studies have been proposed, but these require a number of hardware resources for the parallelism.

\* Corresponding author

SoftTech 2013, ASTL Vol. 19, pp. 191 - 194, 2013 © SERSC 2013 In this paper, we propose an efficient arithmetic based stereo matching method that can reduce the usage of the hardware resources using the unconventional windows and we show that this method can be used to implement the low-cost and highperformance stereo matching system.

### 2 Stereo Matching with 2<sup>k</sup> by 2<sup>k</sup> Window

Local matching methods compute each pixel's disparity independently. The matching cost is aggregated over each disparity range, and the disparity level with the minimal cost is selected as the output of the pixel. Local matching methods utilize color or intensity values within a finite window to determine the disparity.

The performance of the local matching methods depends mainly on the window size. Small windows increase the quality at borders and the localizing of matches is more accurate, but they can cause more false matches. In contrast, large windows are proper for accurate correspondence, but the computational overhead is extremely increased. This paper uses unconventional windows in order to overcome this problem. As shown in Fig. 1, the proposed window has the size of  $2^k$  for width in x and y-axis ( $2^k$  by  $2^k$ ) unlike the conventional window.

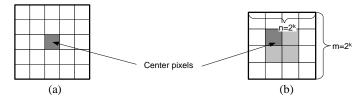


Fig. 1. Local matching windows: (a) conventional window, (b) proposed window.

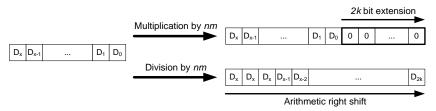


Fig. 2. Multiplication and division replacement by the proposed matching window.

According to the equation of local matching methods, multiplications and divisions by nm, the product of the width and height of the matching window, are included during the computing procedures. These operations require high computational overhead. As shown in Fig. 2, when the proposed window is used for stereo matching, the multiplications by nm can be replaced with the bit extension with 2k numbers of zero and the divisions by nm is also substituted with arithmetic right shift operations. This enables the proposed method to reduce hardware resources compared with conventional window based method.

A Cost-Effective Local Stereo Matching Based on the Unconventional Window

#### **3** Experimental Results

To analyze the impact of the proposed window size on the matching results, the local matching methods are modeled with C. For objective performance comparison, matching results are generated with Middlebury benchmark [4].

Table 1 shows the simulation result images and error rates of local matching methods (SAD and NCC) with the proposed and conventional window sizes. As shown in experimental results, the matching error rate is decreased when the window size is increased. However, when using the similar size of windows, the simulation results for the proposed window size are almost the same with those for the conventional window size. This comparison shows that the proposed method with the unconventional windows has little impact on the matching results compared with conventional window based methods.

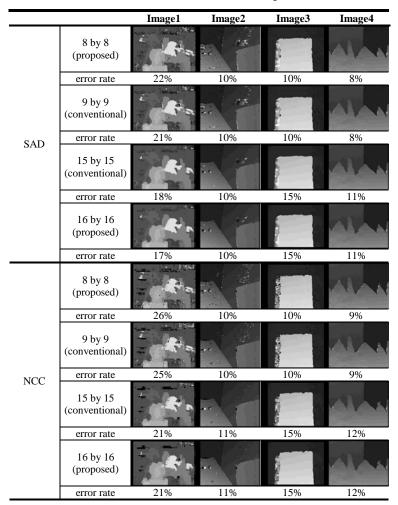


Table 1. Simulation result images

Proceedings, The 2nd International Conference on Software Technology

	16 by 16 (proposed)	15 by 15 (Conventional)
Slice Registers	88,165	105,416
Slice LUTs	71,050	74,320
Fully used LUT-FF pairs	22,304	26,288
DSP 48Es	32	32

Table 2. Usage of hardware resources

Also, for the comparison of the usage of hardware resources between the conventional window based matching and the proposed window based matching, an NCC matching circuit with the proposed unconventional matching window size and one with the conventional window size were designed using Verilog HDL and implemented in Xilinx Virtex5 LX330 FPGA. 15 by 15 and 16 by 16 are used as the conventional and unconventional window sizes, respectively. As shown in Table 2, the usage of hardware resources of the circuit with the unconventional window size of 16 by 16 is much smaller than that of the circuit with the conventional window size of 15 by 15, even though the one has the larger window than the other.

#### 4 Conclusion

Stereo matching methods require large windows for low matching error rate, which they result in high computational complexity. Many multiplications and divisions by the product of the width and height of the matching window are the main factors to increase computational complexity in the conventional window based matching method. This paper proposes the method that uses windows of the size of  $2^k$  by  $2^k$  to implement stereo matching having large windows with low complexity by removing multiplications and divisions by the width and height of the matching window. The experimental results show that the proposed window method can reduce hardware resources without performance reduction compared with the conventional window based method.

Acknowledgments. This research was supported by Basic Science Research Program through the National Research Foundation of Korea (NRF) funded by the ministry of Education, Science and Technology (2011-0013948).

#### References

- Humenberger M., Zinner C., Weber M., Kubinger W., Vincze M.: A Fast Stereo Matching Algorithm Suitable for Embedded Real-time Systems. Comput. Vis. Image. Und. 114(11), 1180--1202 (2010)
- Olson, C.F.: Maximum-Likelihood Image Matching. IEEE Trans. Pattern Anal. Mach. Intell. 24(6), 853--857 (2002)
- Wang, X., Wang, X.: FPGA Based Parallel Architectures for Normalized Cross-Correlation. In: 1st International Conference on Information Science and Engineering, pp. 225--229 (2009)
- 4. Middlebury stereo datasets, http://vision.middlebury.edu/stereo/data/